



Docket No. CISCO-4979 (032590-171)

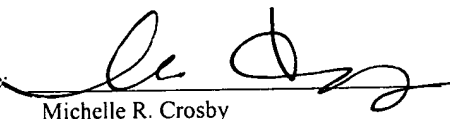
IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT: Baeg, Sang H  
SERIAL NO.: 10/080,145 CONFIRMATION No.: 9291  
FILING DATE: 2/20/02  
TITLE: Test Buffer Design and Interface Mechanism for Differential Receiver  
AC/DC Boundary Scan Test  
EXAMINER: John P. Trimmings  
ART UNIT: 2138

**CERTIFICATE OF MAILING**

I hereby certify that this paper is being deposited with the United States Postal Service as First Class  
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**APPLICANT'S COMMENTS ON REASONS FOR ALLOWANCE**


Applicant gratefully acknowledges the indication of allowance of Claims 20-22, 25-36 (renumbered 1-15). Applicant respectfully urges that the reasons for allowance of Claims 1-15 are not limited to those provided in the Notice of Allowance mailed March 9, 2006 or in other communications from the Patent Office, including any Interview Summary Records, and that additional reasons for allowance may exist, each of which may be independently sufficient to establish the patentability of one or more of Claims 1-15.

Applicant respectfully reserves the right to introduce, articulate, or otherwise comment on any such additional reasons for allowance as may be appropriate in any future proceedings concerning the claimed invention.

Please charge any additional required fee or credit any overpayment to our deposit account number 50-1698.

Respectfully submitted,  
THELEN REID & PRIEST LLP

Dated: 6/9/06

  
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